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Title of Invention

METHOD TO SELECTIVELY CORRECT CRITICAL DIMENSION ERRORS IN THE SEMICONDUCTOR INDUSTRY

Application Number:

Date:

First Named Applicant: Jed H. Rankin

Confirmation Number:

Attorney Docket Number: BUR920040086US1

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Submitted by:	Elec. Sign.	Sign. Capacity
William D. Sabo Registered Number: 27465	/William D. SABO, Reg.# 27465/	Attorney

Documents being submitted

us-request

Files

BUR920040086US1-usrequ.xml

us-request.dtd

us-request.xsl

us-assignment

BUR920040086US1-usassn.xml

us-assignment.xsl

us-assignment.dtd

BUR920040086US1Assign1.tif

BUR920040086US1Assign2.tif

us-fee-sheet

BUR920040086US1-usfees.xml

us-fee-sheet.xsl

us-fee-sheet.dtd

us-ids

BUR920040086US1-usidst.xml

us-ids.dtd

us-ids.xsl

application-body

BUR920040086spec.xml

application-body.dtd
wipo.ent
mathml2.dtd
mathml2-qname-1.mod
isoamsa.ent
isoamsb.ent
isoamsc.ent
isoamsn.ent
isoamso.ent
isoamsr.ent
isogr3.ent
isomfrk.ent
isomopf.ent
isomscr.ent
isotech.ent
isobox.ent
isocyr1.ent
isocyr2.ent
isodia.ent
isolat1.ent
isolat2.ent
isonum.ent
isopub.ent
mmlextra.ent
mmlalias.ent
soextblx.dtd
us-application-body.xsl
Image1.tif
Image2.tif
Image3.tif
Image4.tif
Image5.tif
Image6.tif
Image7.tif
Image8.tif
BUR920040086US1Dec2.tif
BUR920040086US1dec1.tif

us-declaration

us-declaration

Comments

Assignee Address: International Business Machines Corporation, Armonk, NY 10504